EXPEDITED PROCEDURE – EXAMINING GROUP 2818

PATENT

S/N 10/732,949
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

 Applicant:
 Duncan Kitchen et al.
 Examiner: Phuc T. Dang

 Serial No.:
 10/732,949
 Group Art Unit: 2818

 Filed:
 December 11 2003
 Docket No.: 884 G25 US1

Filed: December 11, 2003 Docket No.: 884.G25US1
Title: METHOD AND APPARATUS FOR MANUFACTURING A TRANSISTOR-

OUTLINE (TO) CAN HAVING A CERAMIC HEADER

Assignee: Intel Corporation Customer Number: 21186

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In response to the Final Office Action mailed February 14, 2006, please amend the application as follows:

This response is accompanied by a Petition, as well as the appropriate fee, to obtain a one-month extension of the period for responding to the Office action, thereby moving the deadline for response from May 14, 2006 to June 14, 2006.